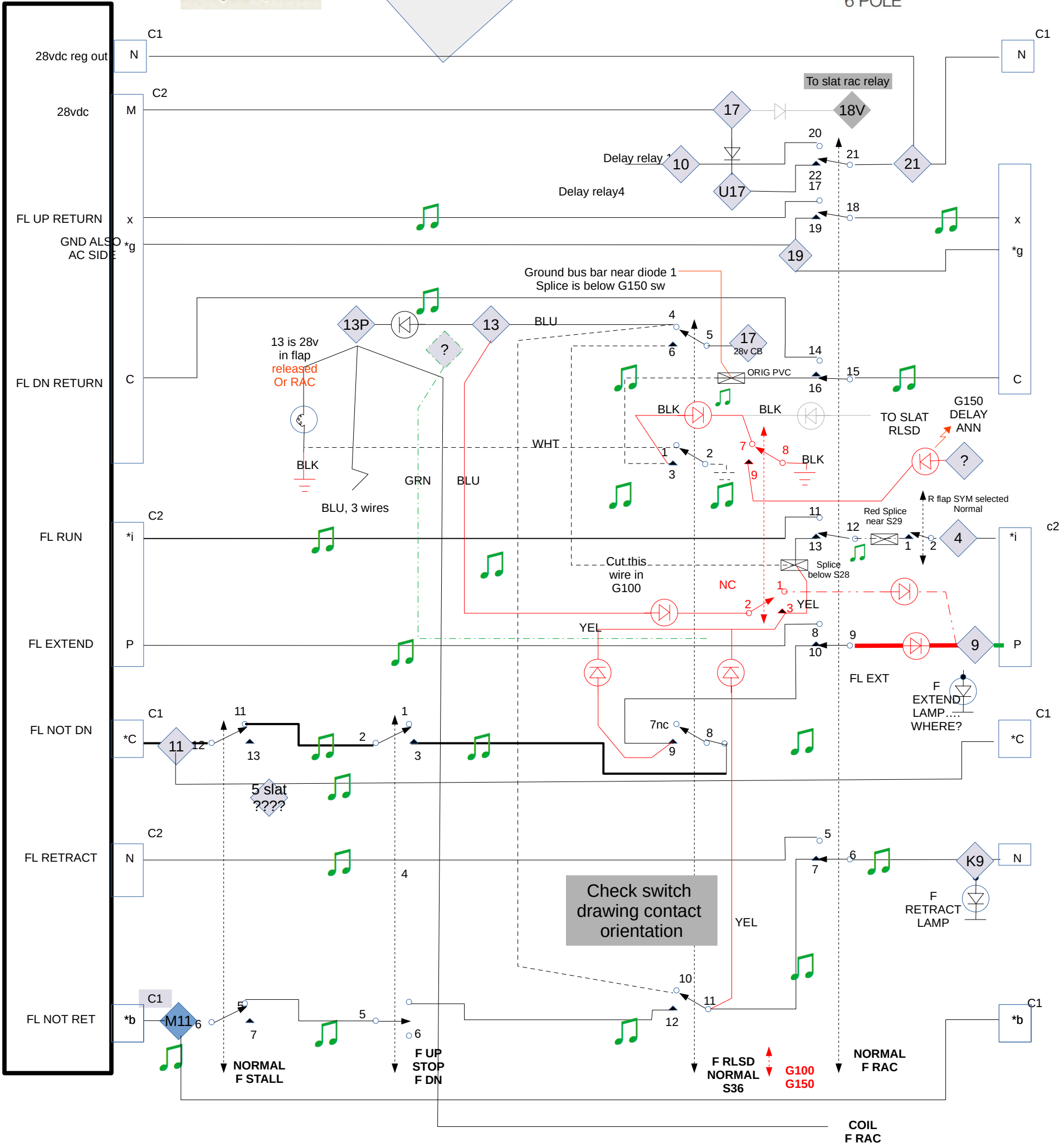
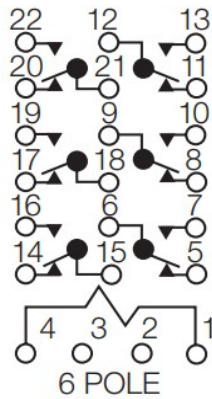
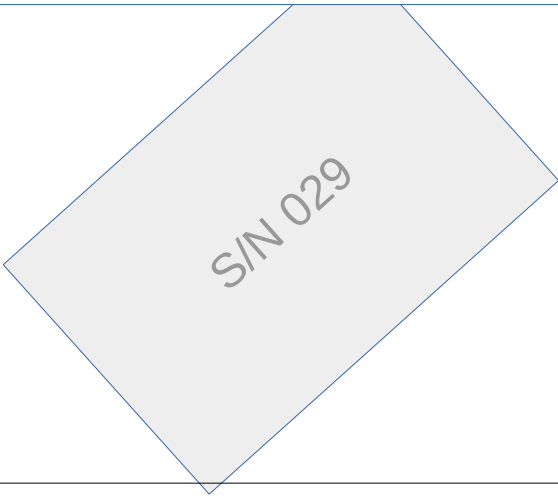
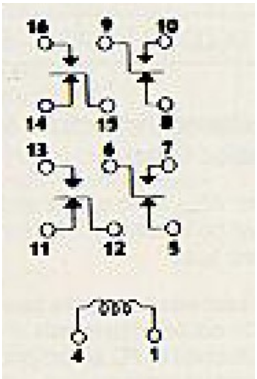
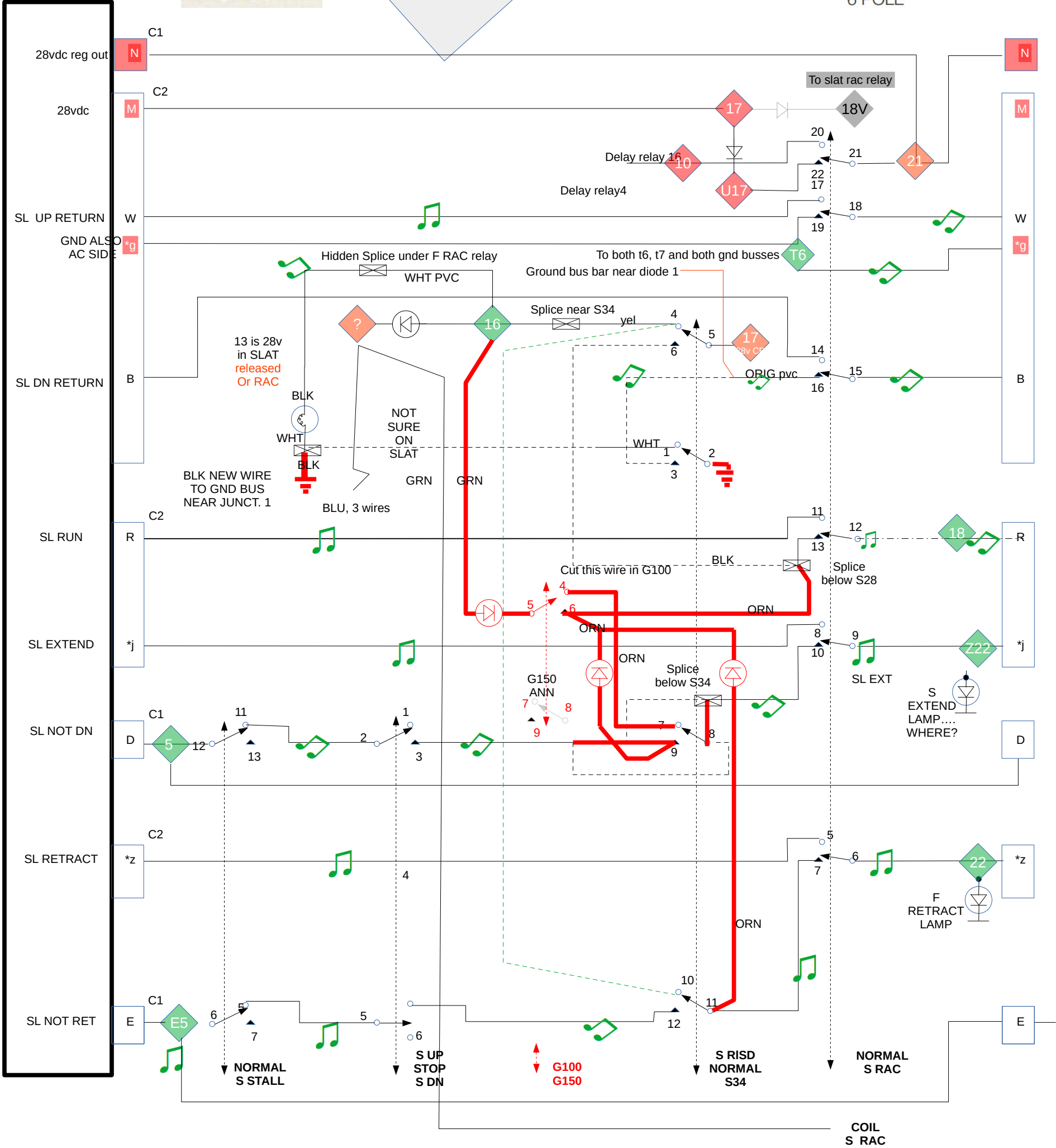
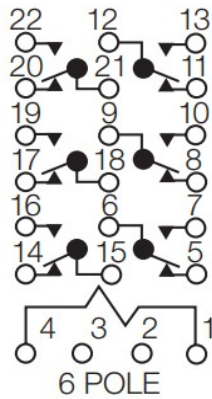
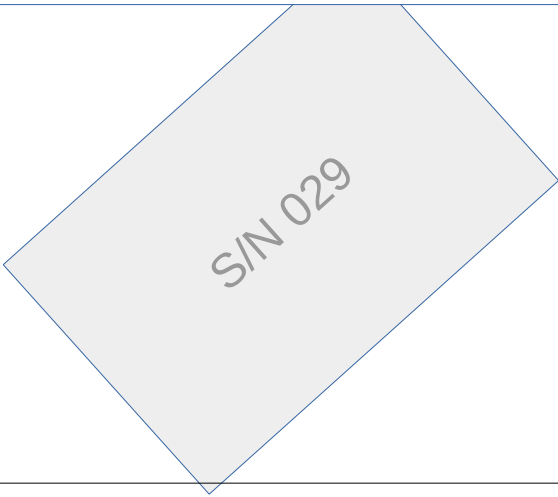
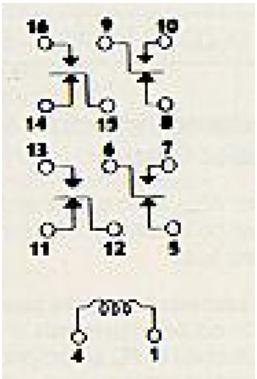




Red lines
are added
wires for
G150
delay

Dashed
lines are
original
wires to
be
removed

FLAP RAC CONTROL



SLAT DOWN RETURN IS NOT
ACTIVE IN S RLSD WHEN MOD
BEGAN

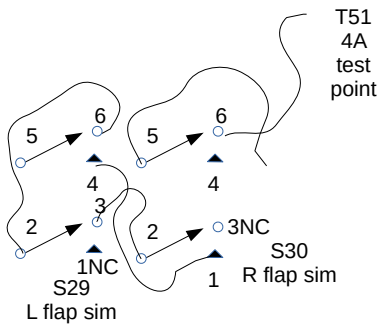
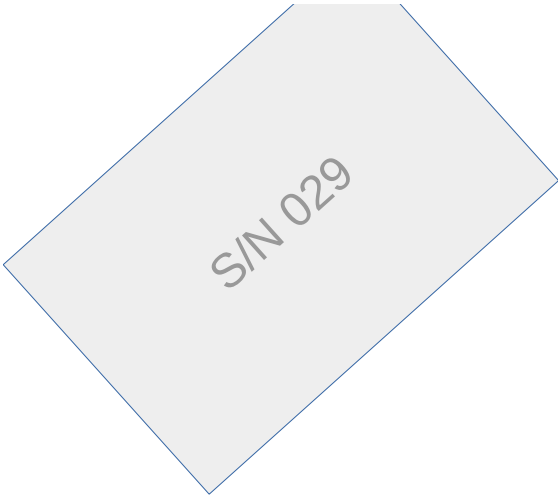
STRAIGHT NOTE
IS COPY /PASTE
FROM FLAP
CIRCUIT

SLANTED NOTE IS
RUNG OUT ON
SLAT CIRCUIT

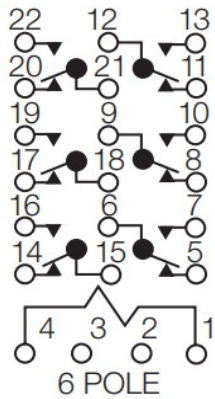
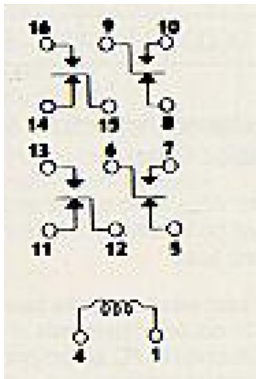
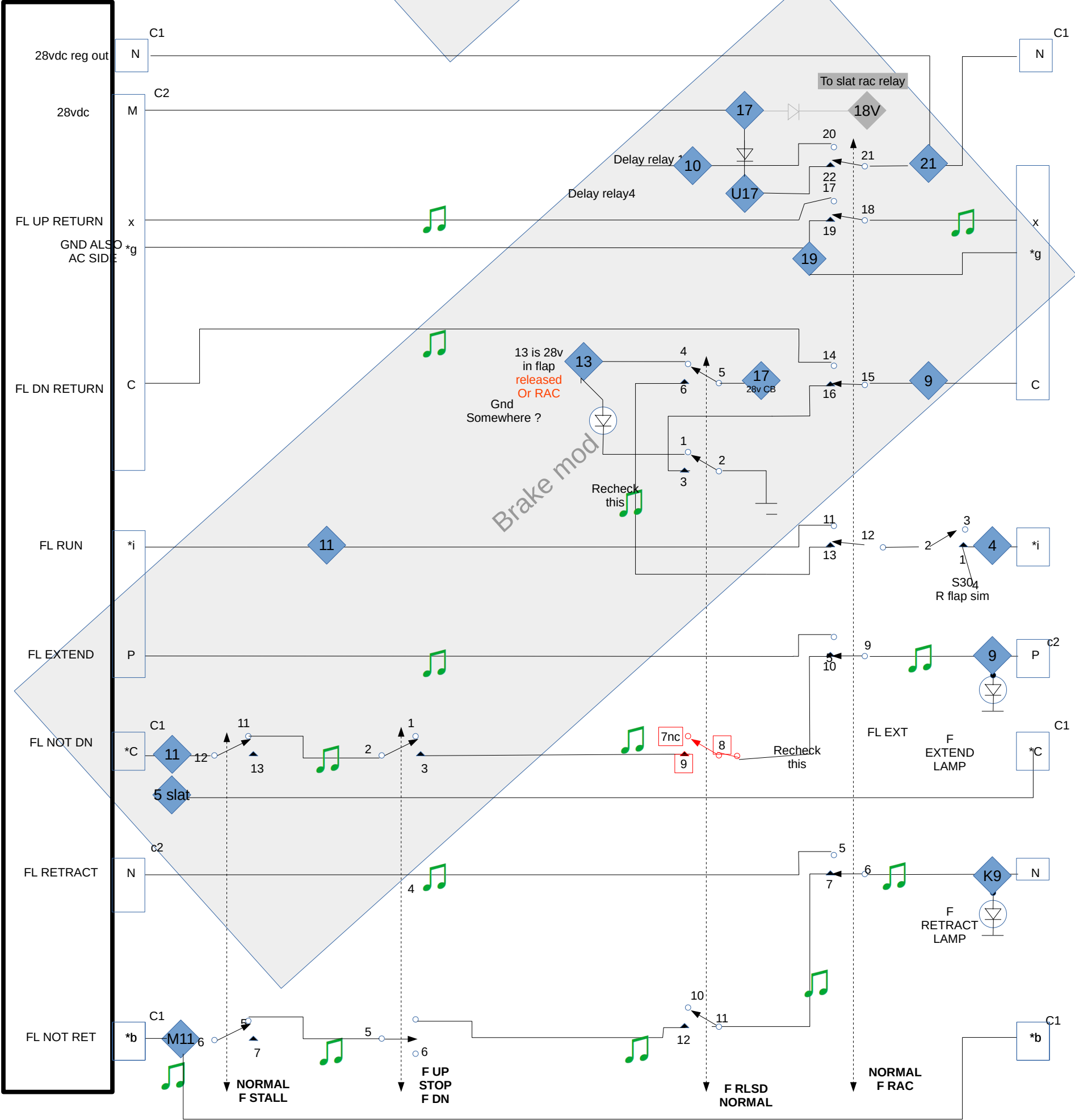
Red lines
are added
wires for
G150
delay

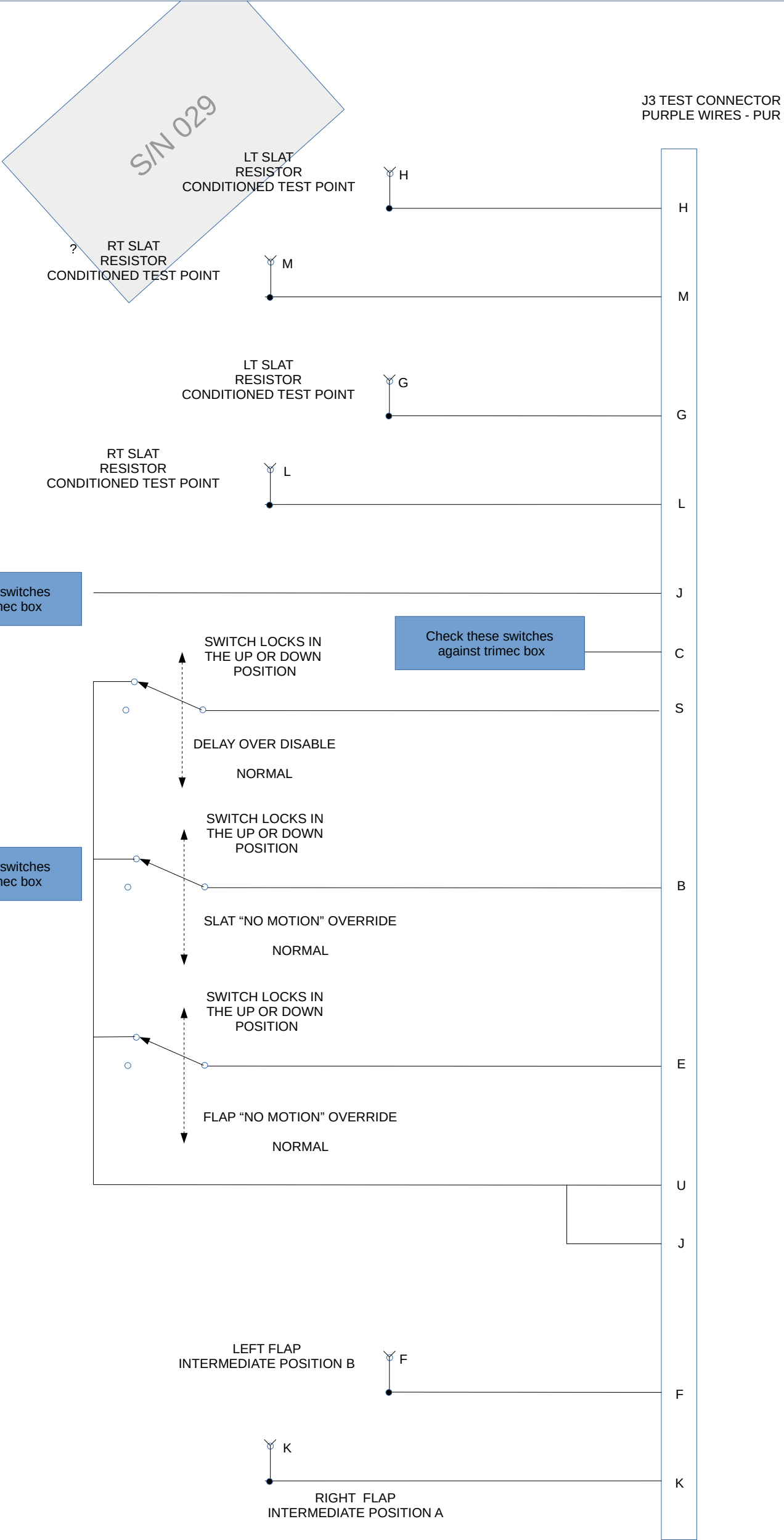
Dashed
lines are
original
wires to
be
removed

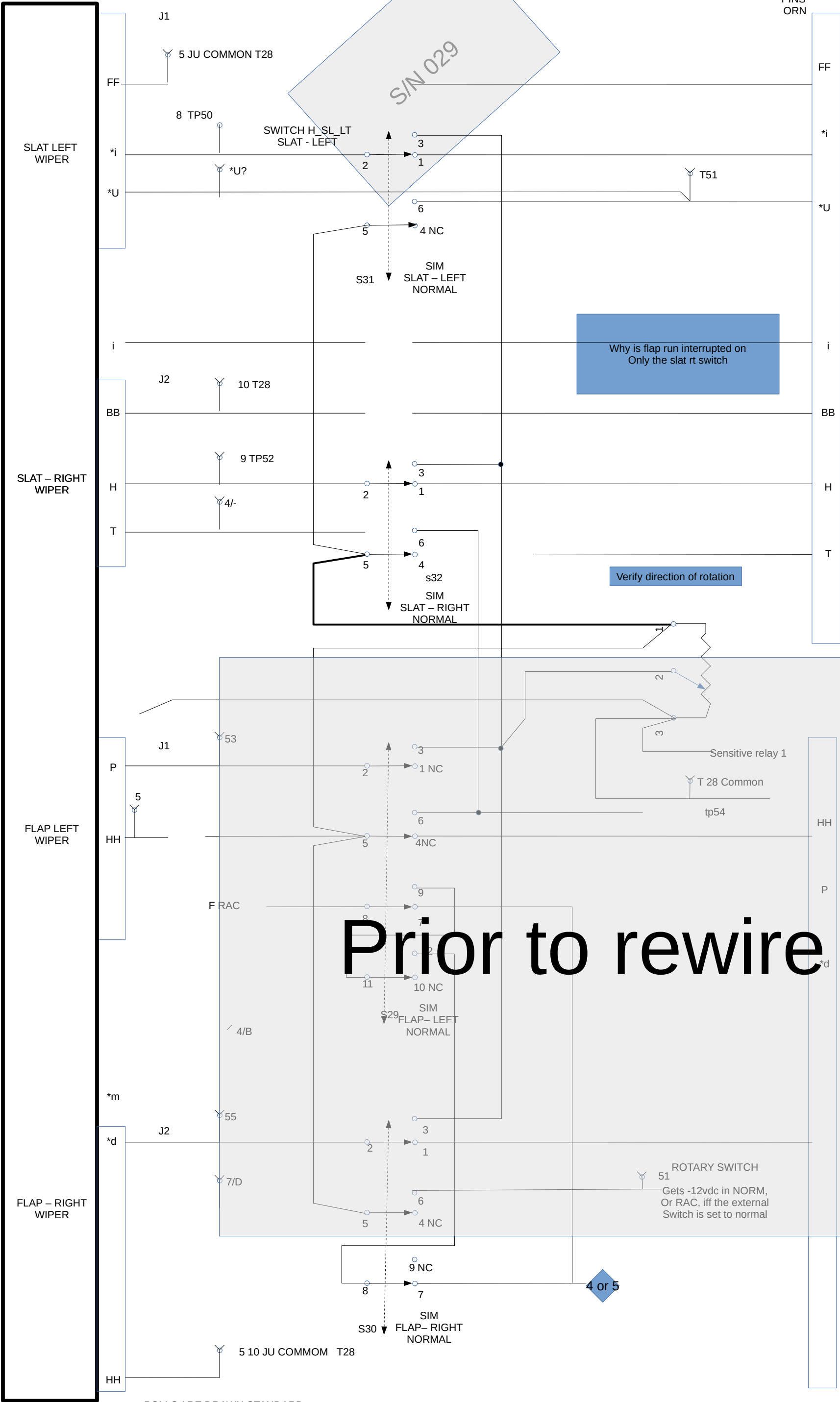
SLAT RAC CONTROL



S/N 029







SLAT – RIGHT WIPER

B -12VDC

FLAP – RIGHT WIPER

A -12VDC

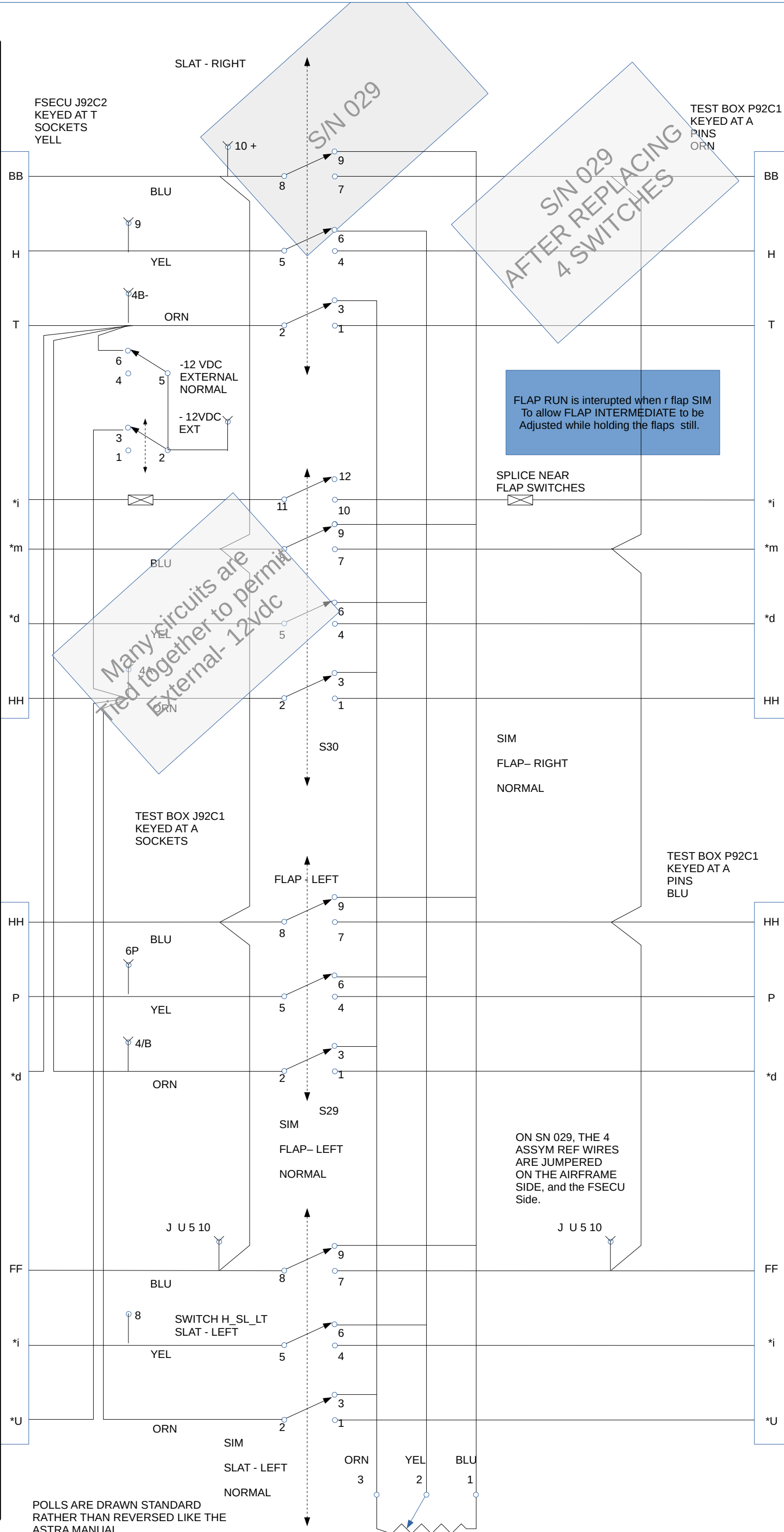
FSECU J92C1 KEYED AT A PINS

FLAP LEFT WIPER

B -12VDC

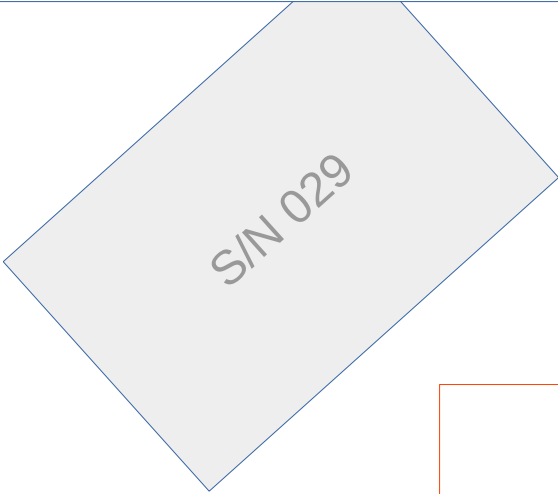
SLAT LEFT WIPER

A -12VDC

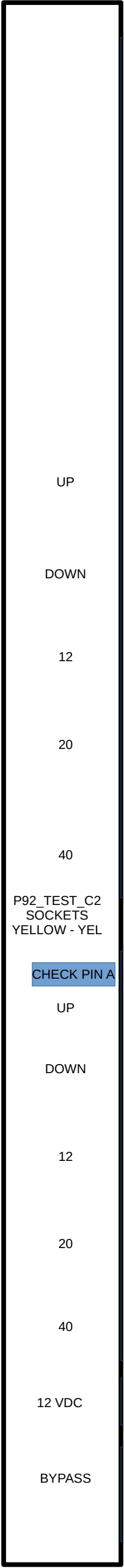


POLLS ARE DRAWN STANDARD RATHER THAN REVERSED LIKE THE ASTRA MANUAL.

Also, FSECU is drawn on the left here , it is on the right in the original



P92_TEST_C1
SOCKETS
GREEN WIRES



J92C1

COCKPIT SL UP

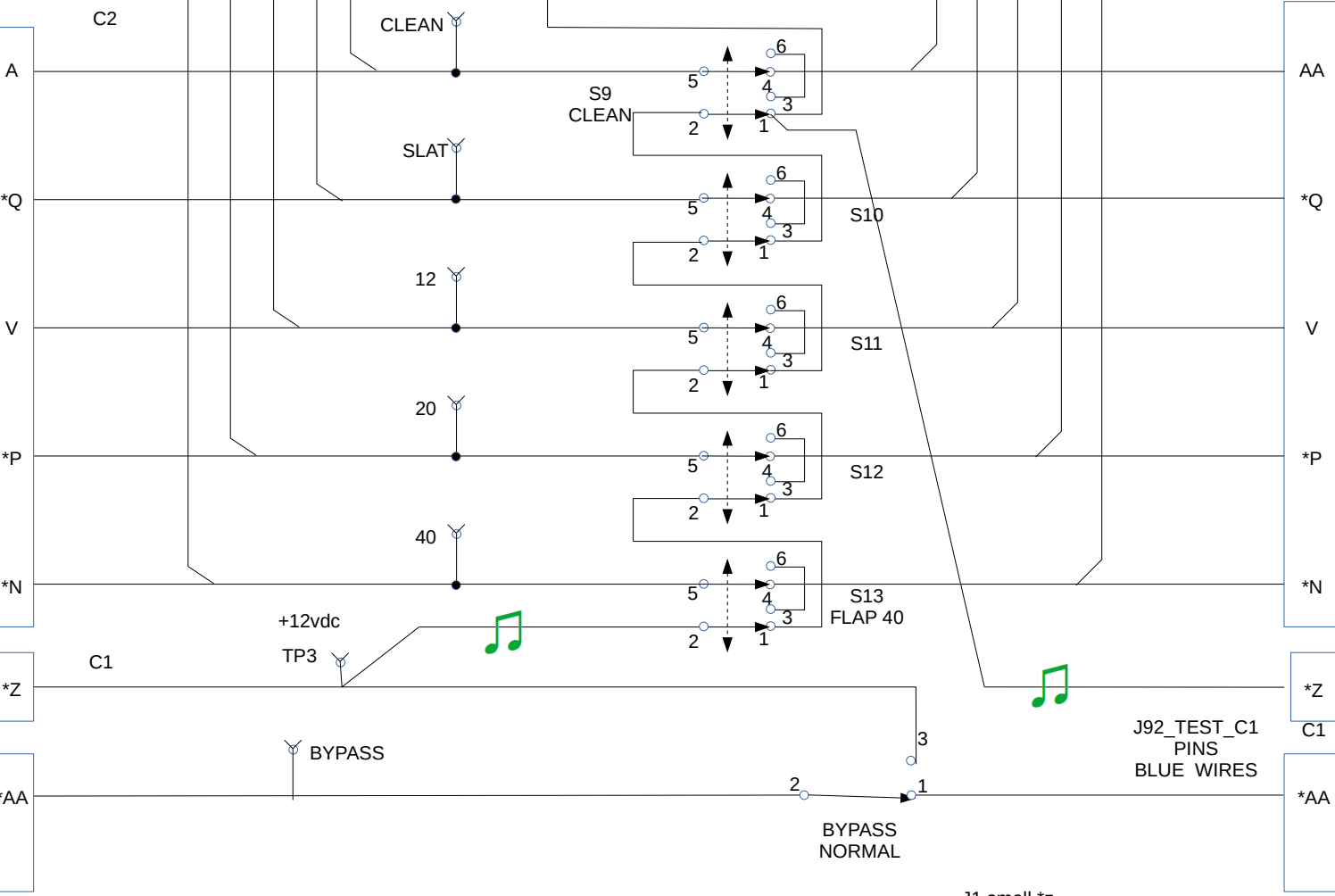
COCKPIT SL DN

COCKPIT FL 12

COCKPIT FL 20

COCKPIT FL 40

S3 copilot msi pin 1
S7 valid pin 3
S7 valid pin 3



COCKPIT SL UP

COCKPIT SL DN

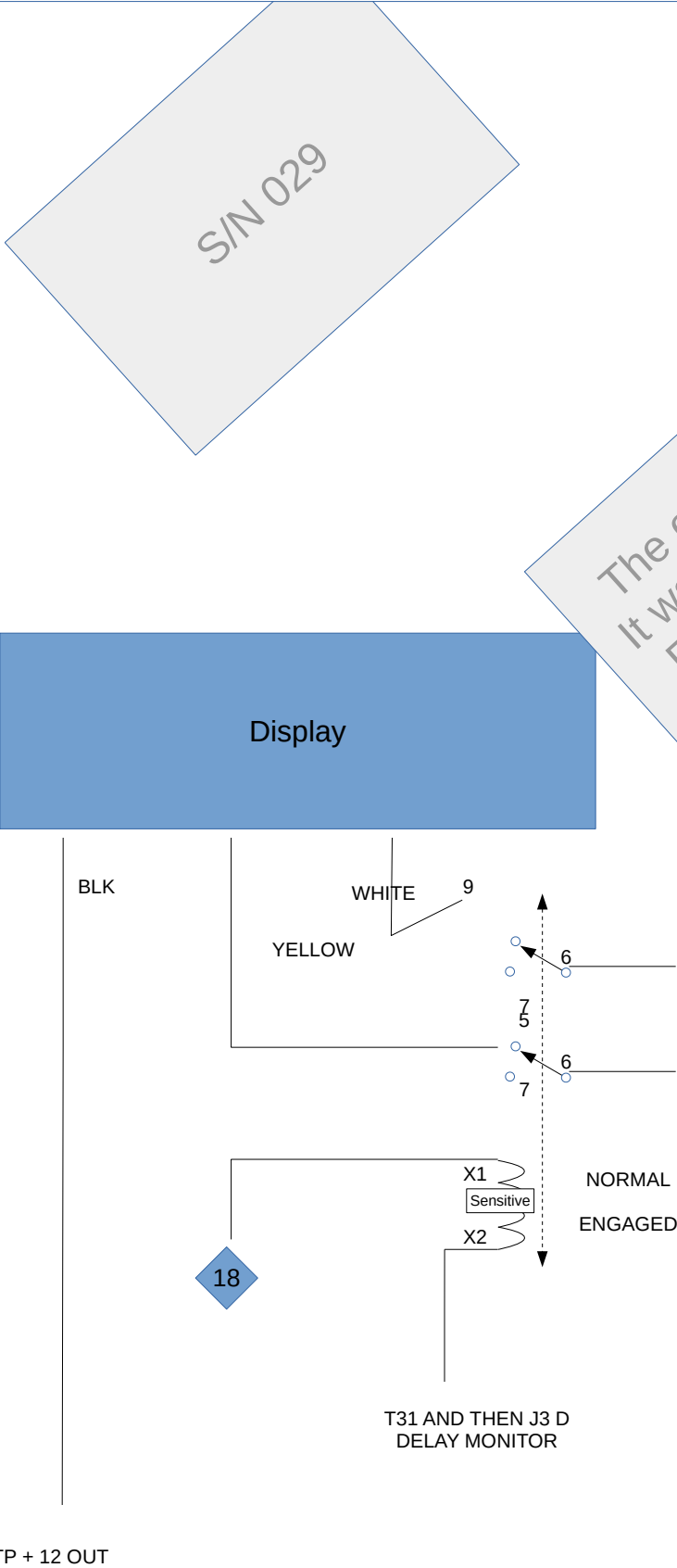
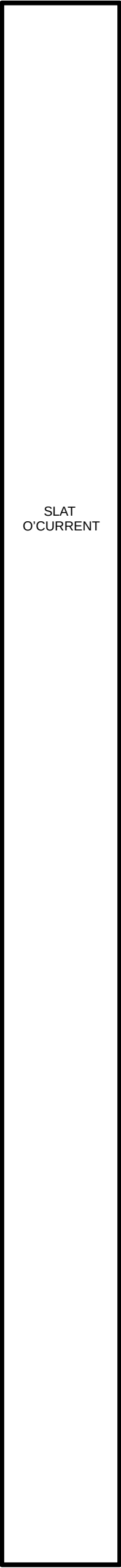
COCKPIT FL 12

COCKPIT FL 20

COCKPIT FL 40

C1

BYPASS

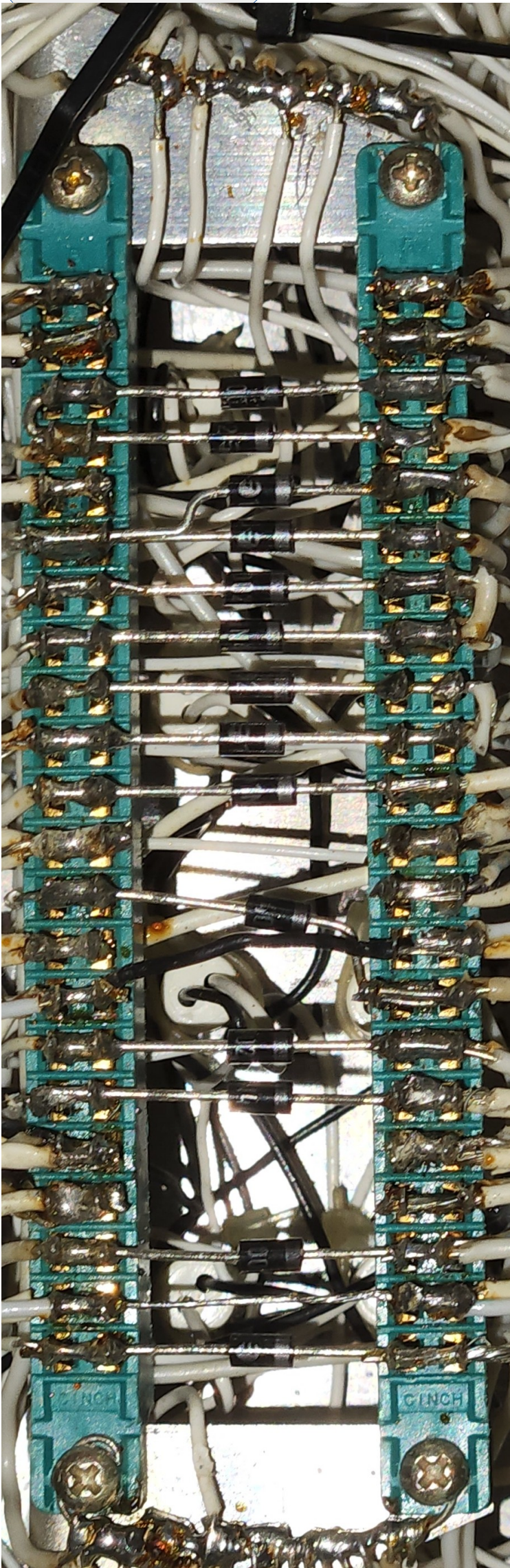


The old display is deprecated.
It watched C3- pin D for FSECU
FAULT TIMING. 1.25 seconds

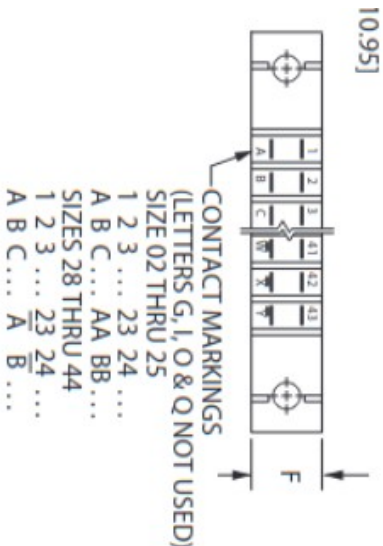


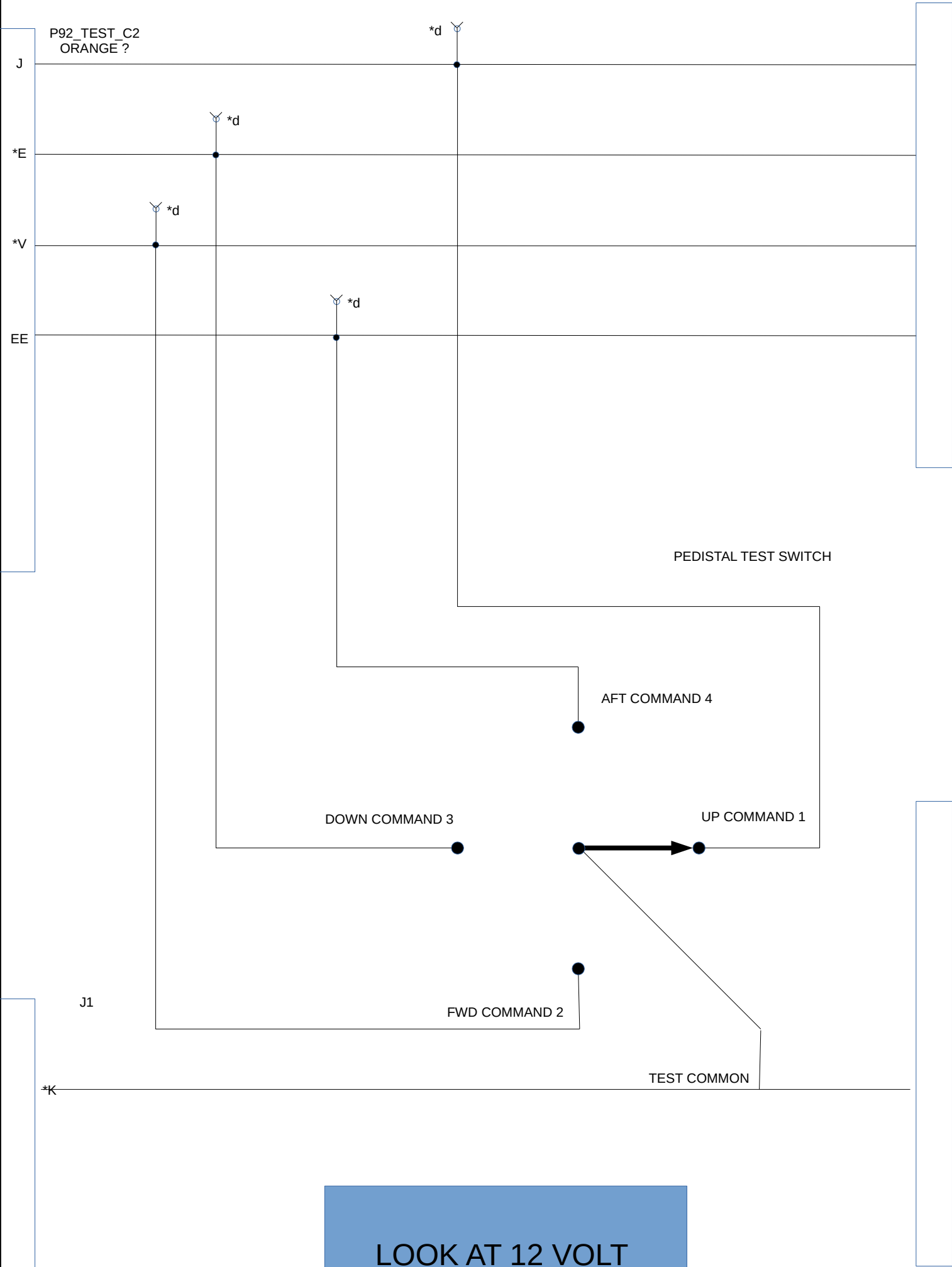
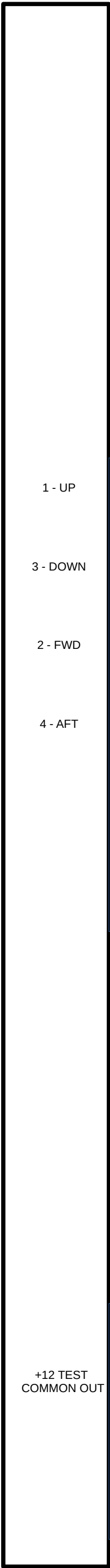
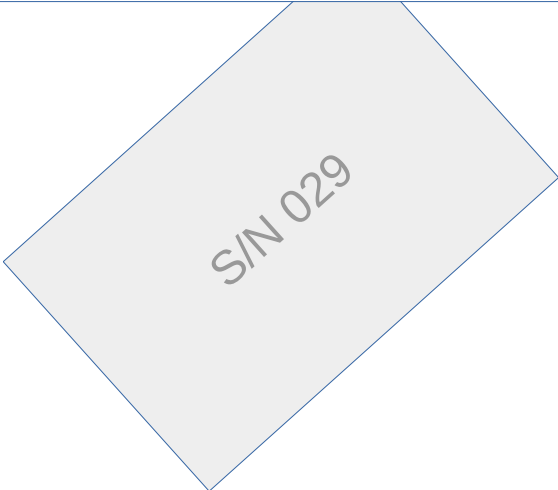
S/N 029

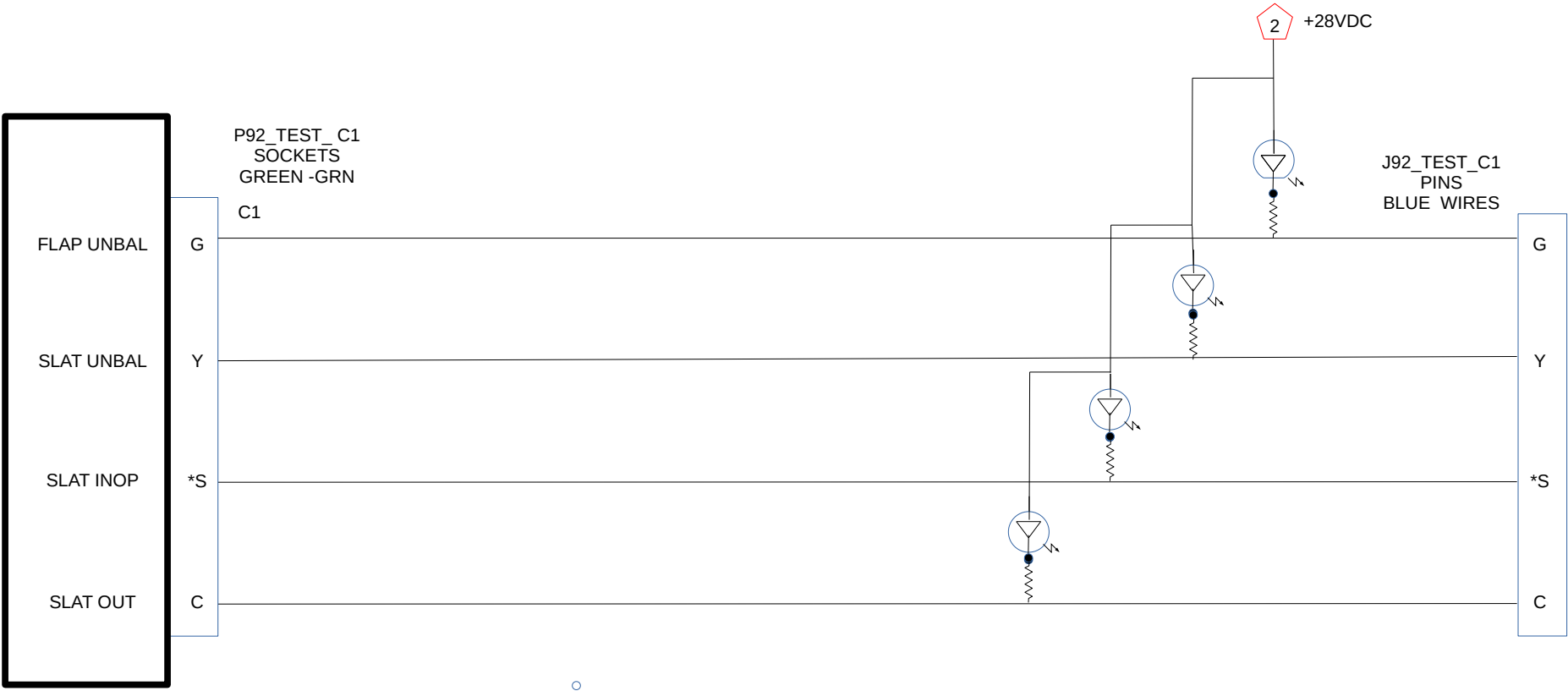
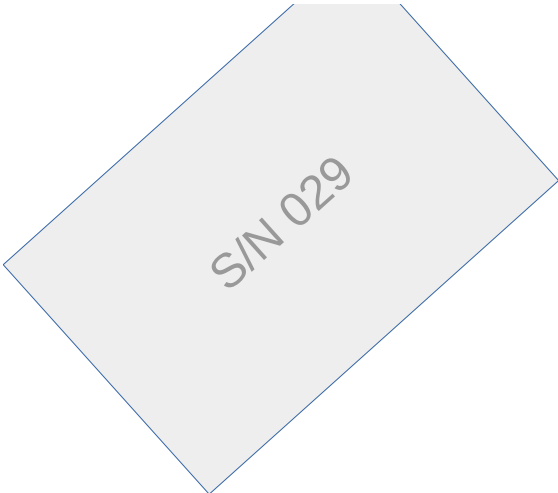
22 J2AC *z SL NOT UP
21
20
19 sensitive relay 5
18 SL RUN
17 ship side J2 M, 28 VDC from CB
16 28 v in slat rlstd, added green slat 28v
15 28v in slat rac from rotary sw
14 28v in slat rack???????
13 28v in FLAP RLSD to power ann, S33-10- now blue wire also I33
12
11 J1AC *c FL NOT FULL DN
10 flap dely 4
9 J2AC - P FLAP DN CMD, FRAC relay 21
8 SLAT CURRENT to test point
7
6
5 SL NOT FULLY EXT - J1AC – D ALSO J1 FSECU
4 BRK 28V, *i rang to acft wire broken, or s55-1 rh flap asym sw, ALSO SLAT STAL RELAY 12,
3 slat delay relay pin 4, x2
2 FLAP CURRENT
1



22Z J2 AC SLAT NOT EXTENDED
21Y pin 21 on flap rac relay
20X
19W
18 V
17 U , FRAC relay 22
16 T
15 S
14 R
13 P
12 N
11 M Flaps not fully up fsecu and airframe*b provides power for F RUN UP , FSCB J1*j
10L J2AC – N, also flap up light
9K
8J
7H
6F
5E J1AC SL NOT FULLY RETRACT ALSO J1FSECU
4D
3C
2B SLAT STALL *a J1
1A







CONNECTIONS TO PCB

FROM PAGE 2 28VDC, GND REF -12VDC,

FROM PAGE 3: RAC CIRCUITS, ASSYM LOCKOUT

FROM PAGE 5: ASSYM LOCKOUT

FROM PAGE 6: NA

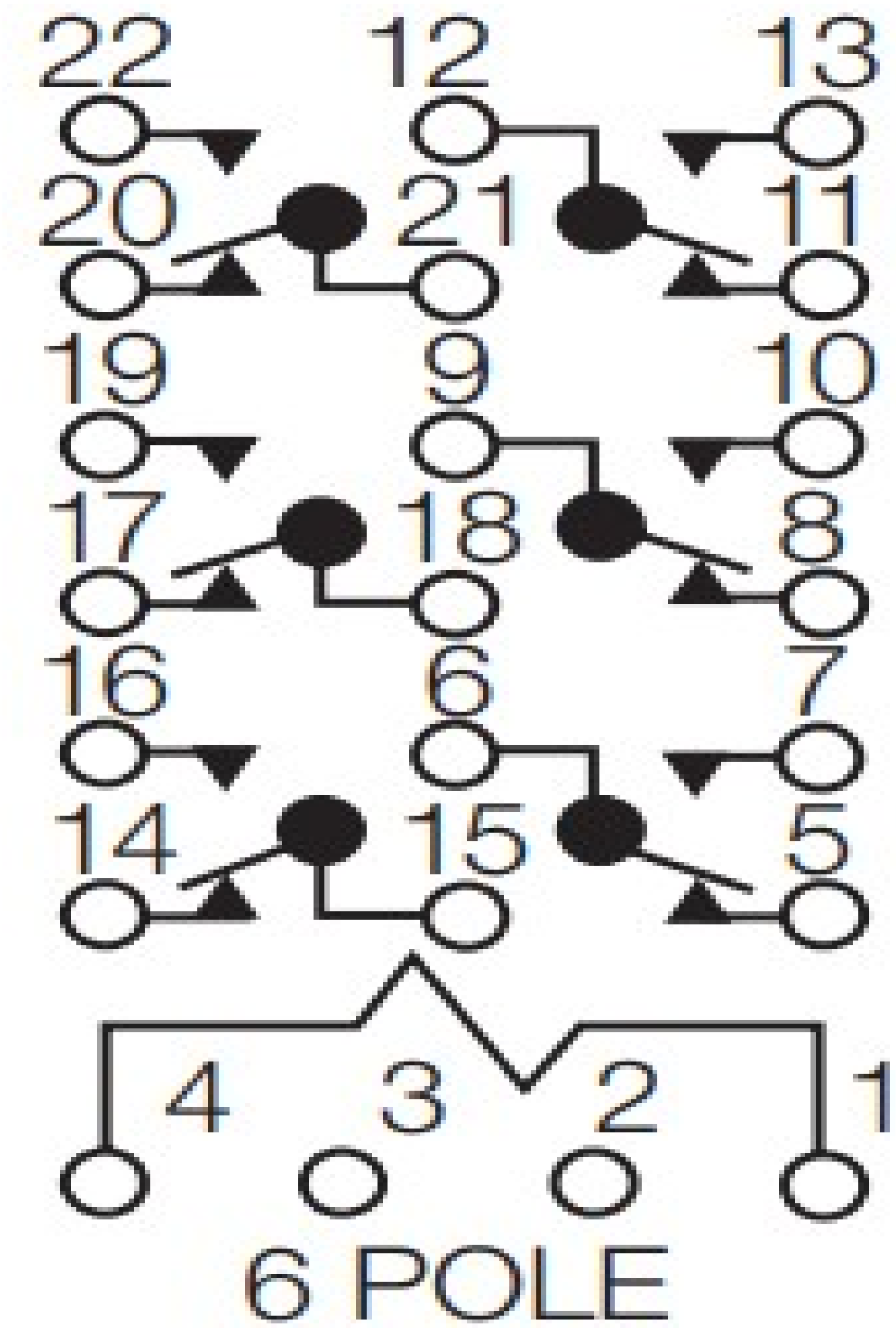
FROM PAGE 7: PDU O HEAT, PPDU O'CURRENT

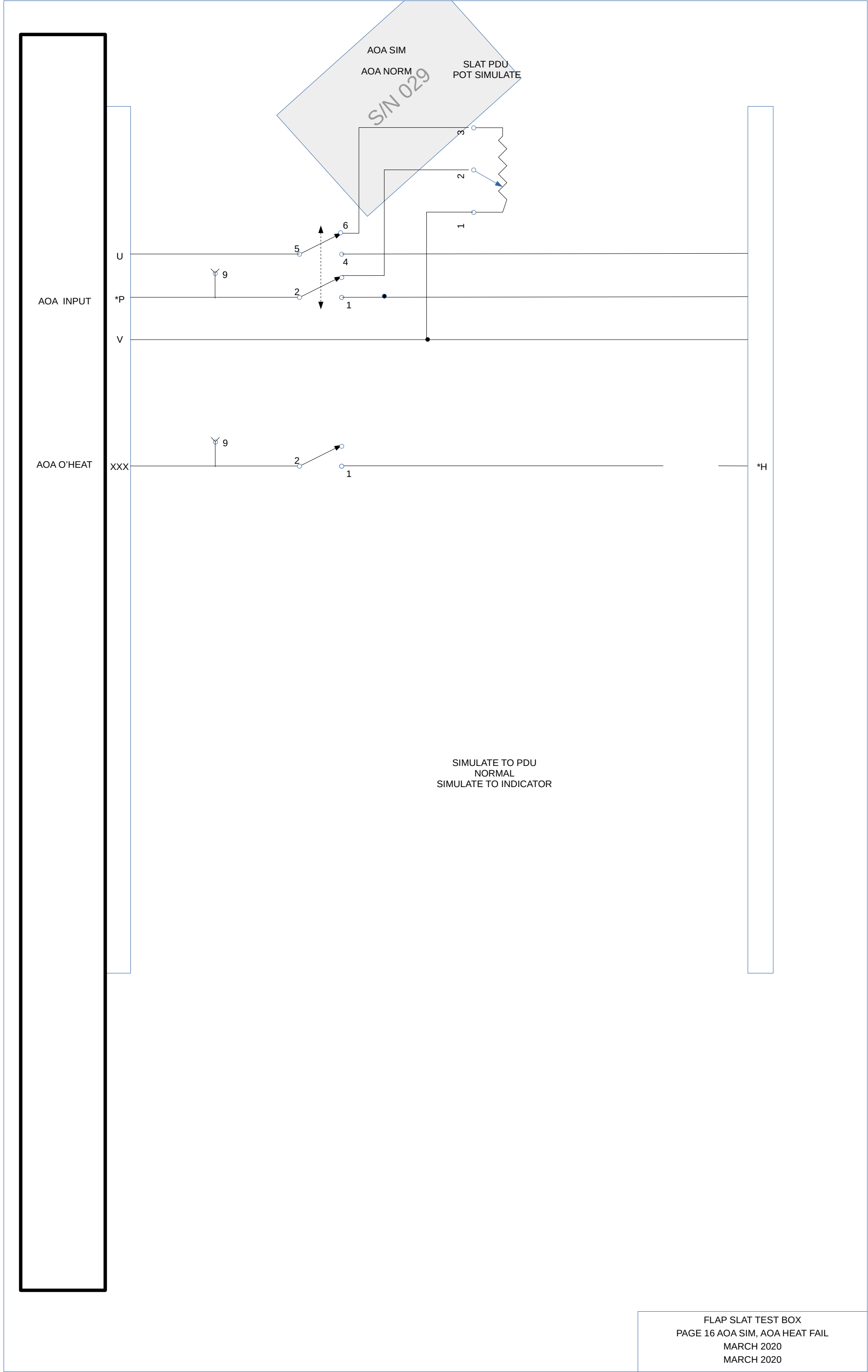
FROM PAGE 8: PDU RESSITOR SIM CIRCUITS

FROM PAGE 9: DIOES INTRERNAL TO THE PCB

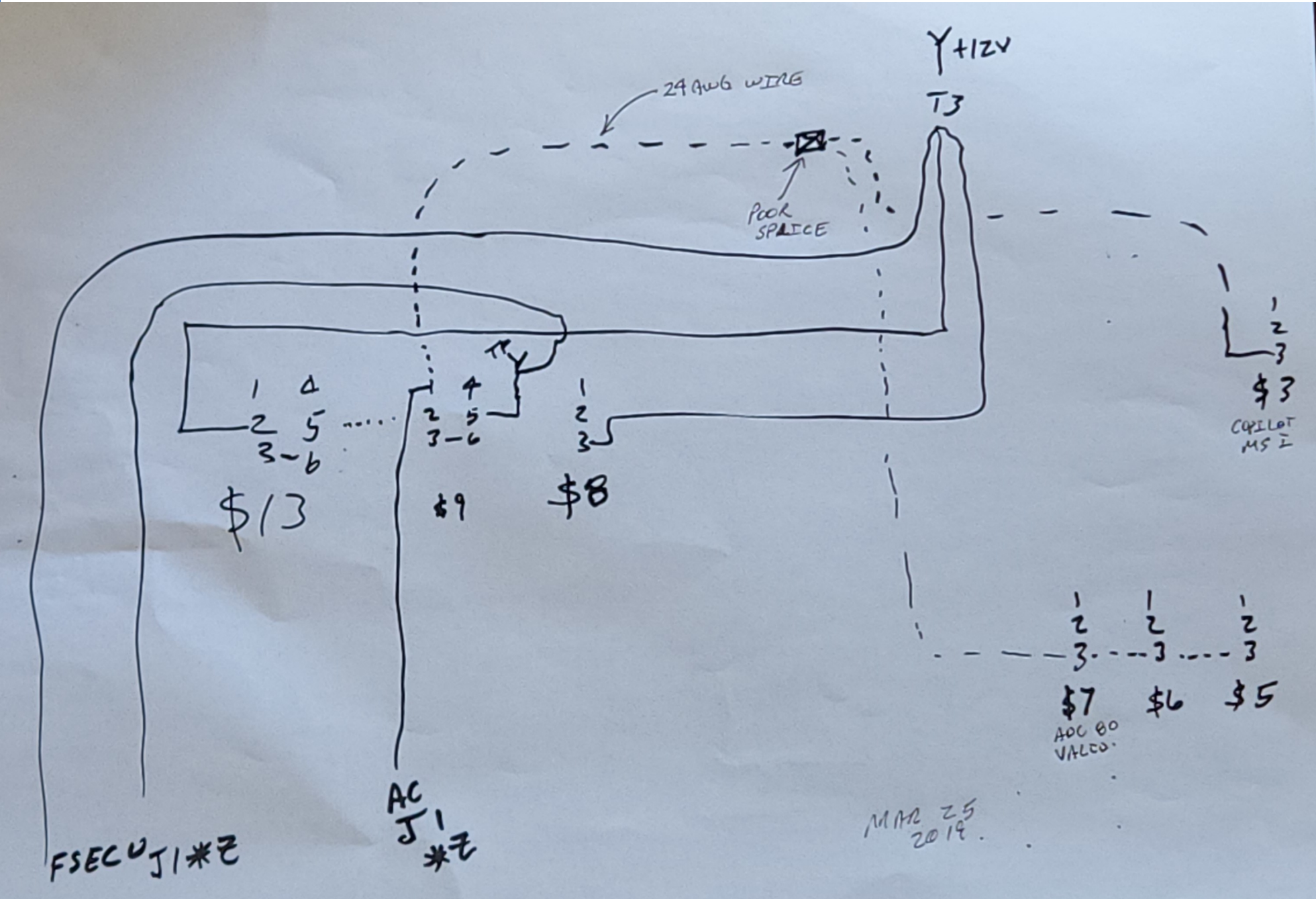
FROM PAGE 10:

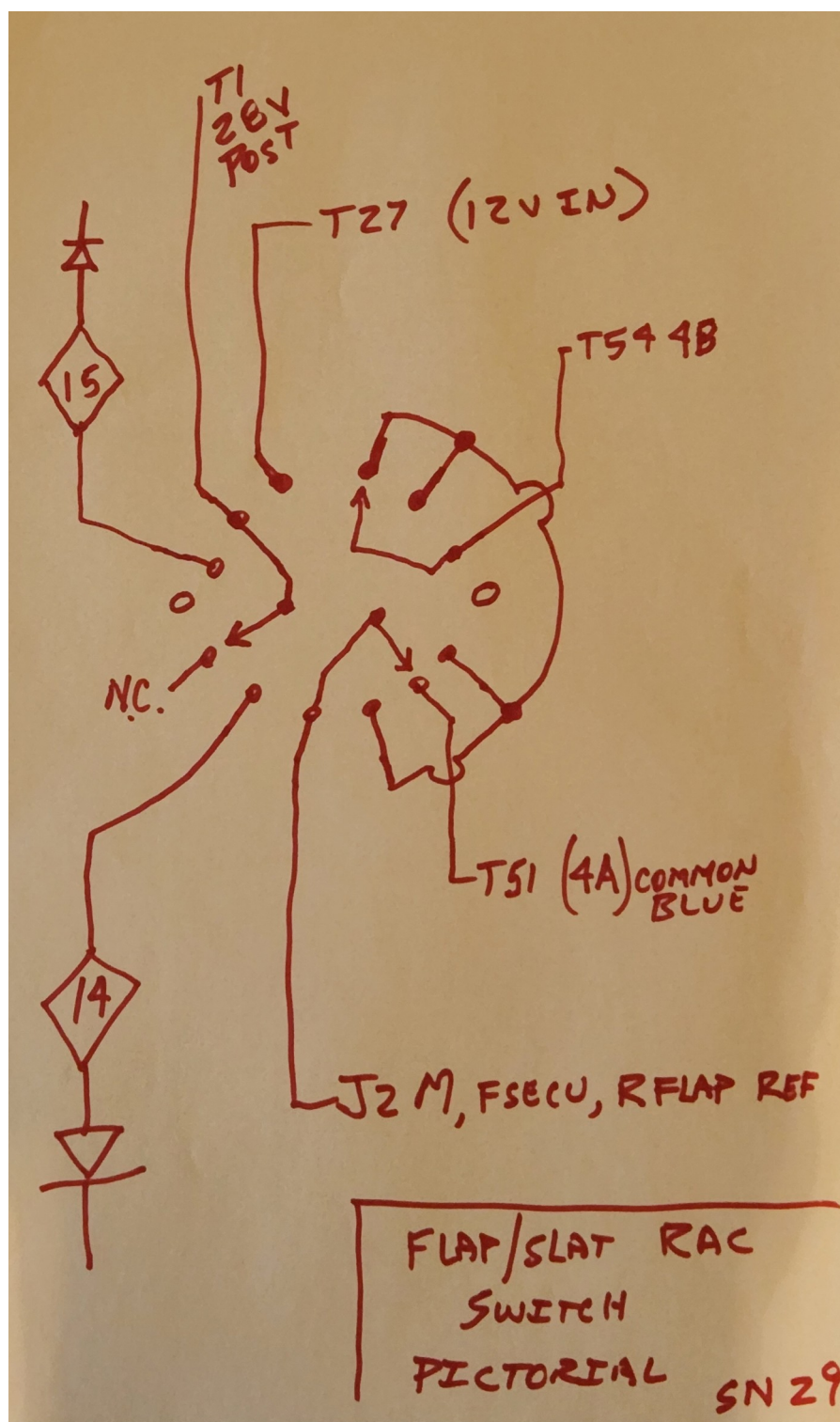
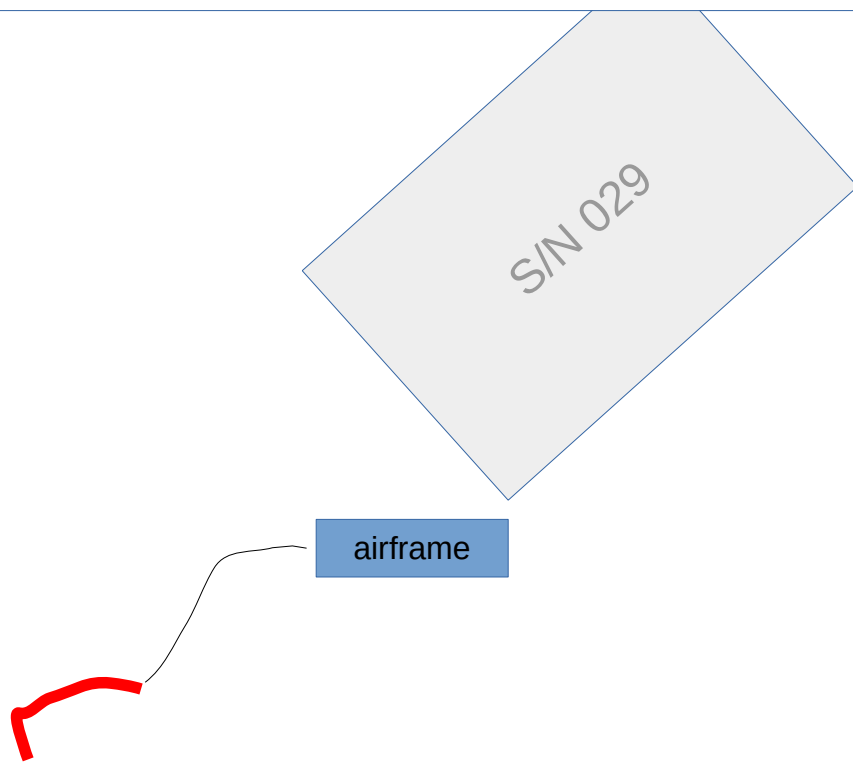
S/N 029

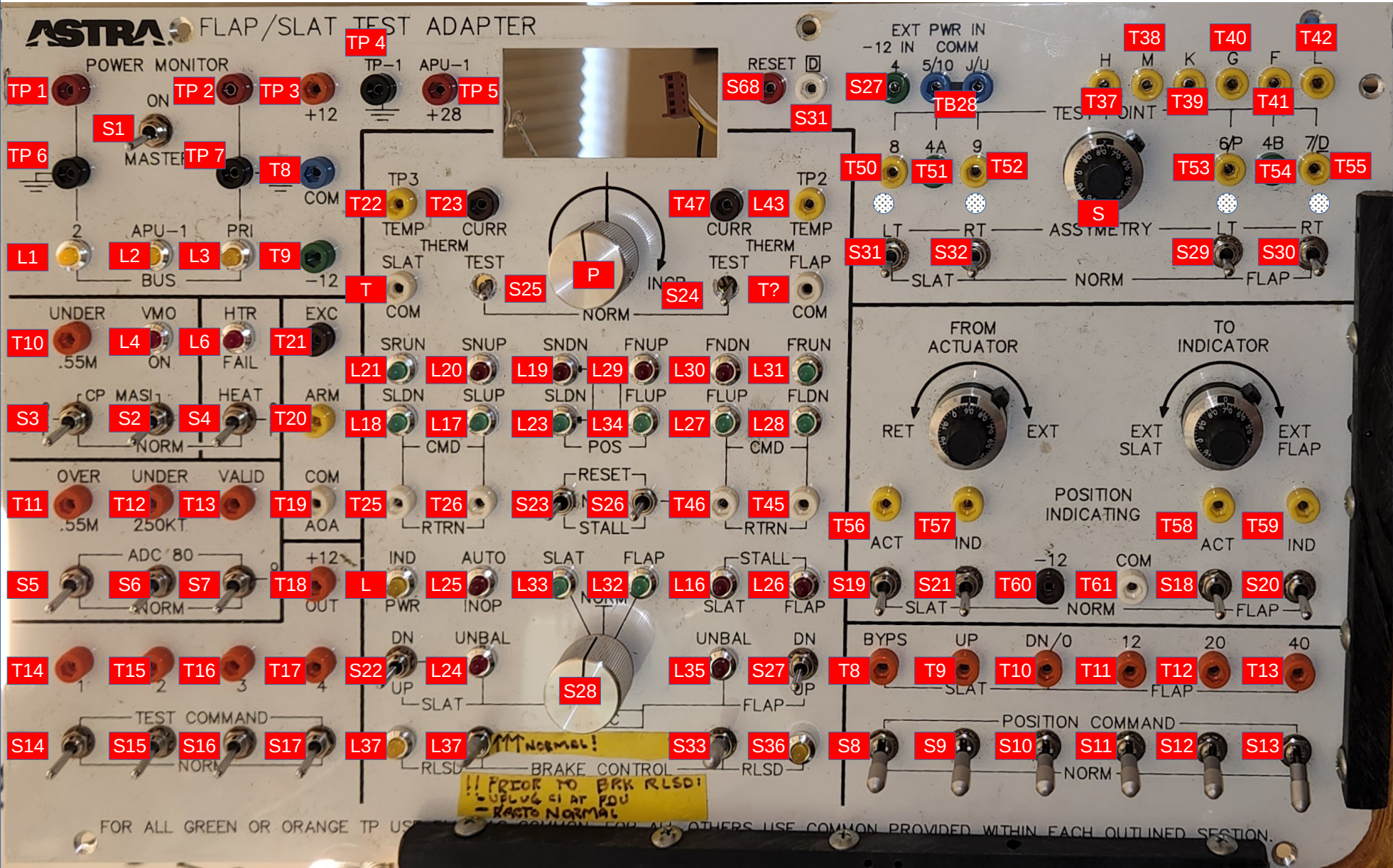




S/N 029



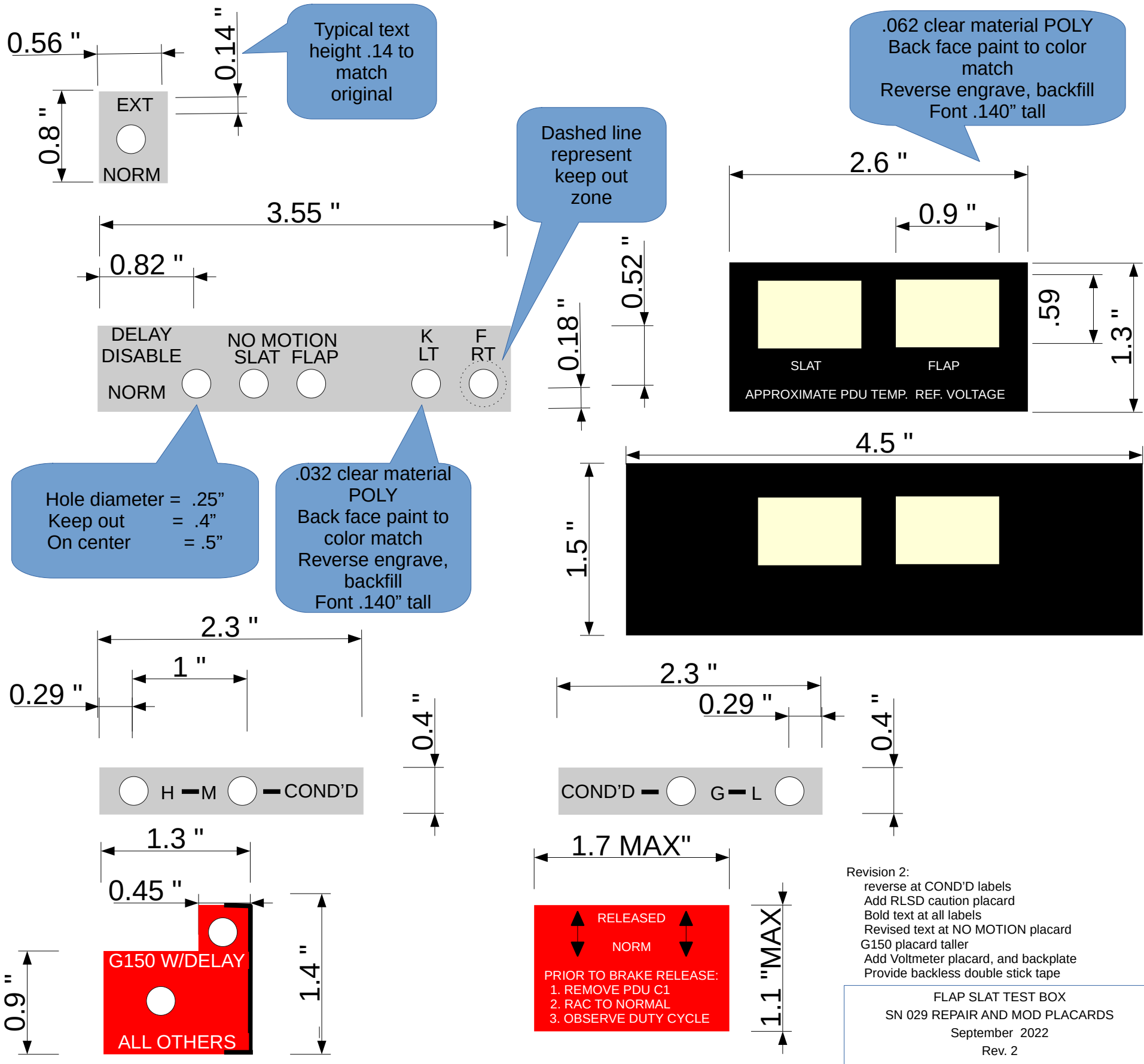




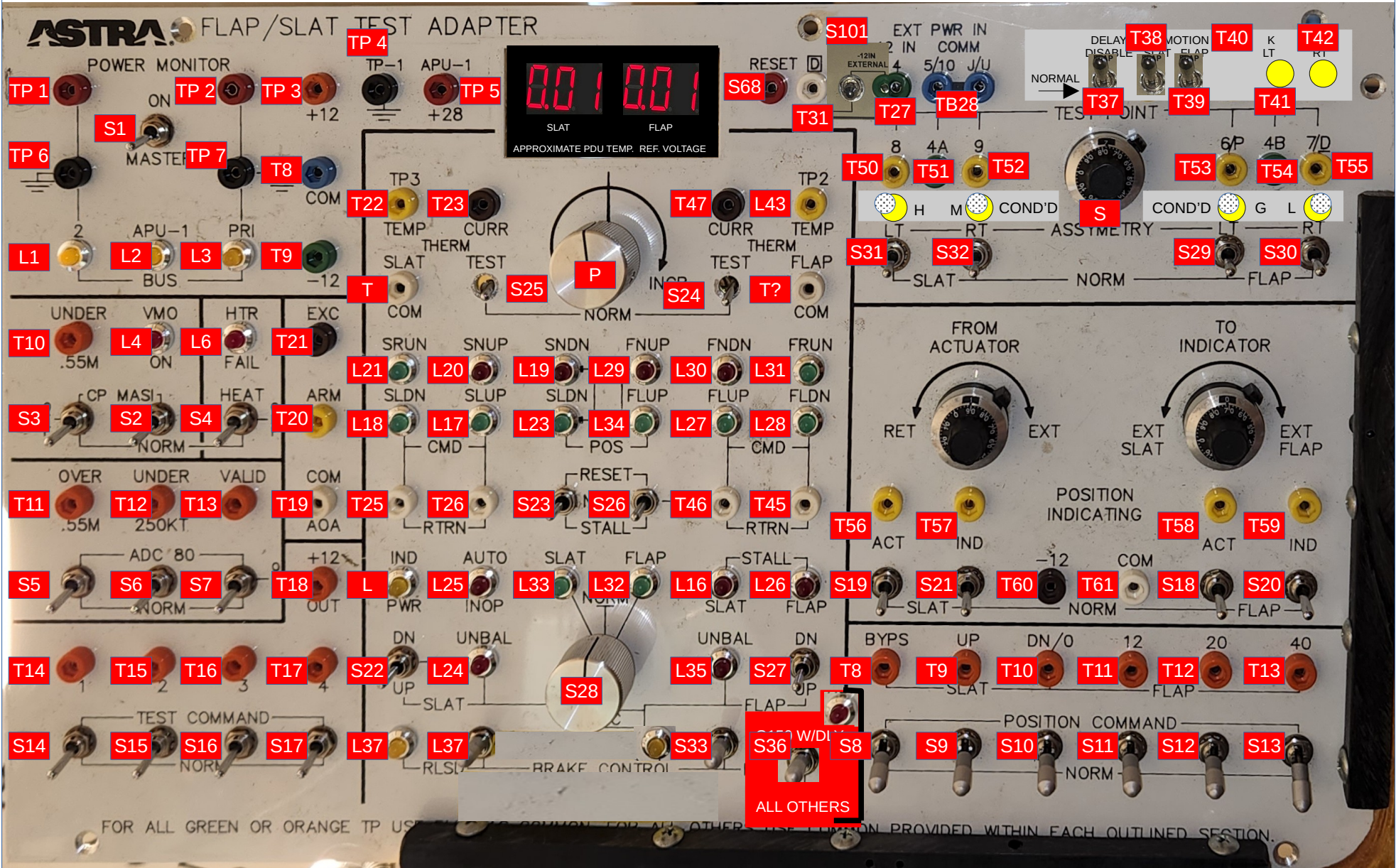
s1	0.003	
s2		
s3		loose
s4	0.004	
s5	2	
s6	2	
s7	2.5	
s8	0.02	
s9	0.05	
s10	0.2	
s11	0.029	
s12		
s13	0.5	loose

Substituted with 3 position switch

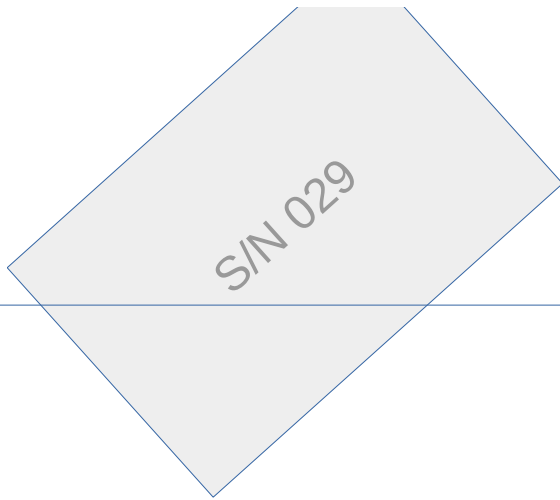
s18	1.5	
s19	0.008	
s20	.25	
s21	0.008	
s22	0.004	
s23	0.073	
s24	.039	
s25	0.109	
s27	0.004	
s28	0.027	upper terminal erratic
s29	0.055	
s31	0.066	
s32	0.125	
s33	0.002	
s34	0.002	



POWER AND GROUND ANALYSIS



TP		DESCRIPTION
3	J1 BB	UNLABELED +12 VDC ON FS CHEAT SHEET
8		+12 TO FLAP, BYPASS, AOA, MACH
4		ALL POWER GROUNDS + FACE JUMPER ON SN 074 BOX
5		
6		RETURN
7		RETURN
28		ALL SLAT ASSYM COMMON
51		LEFT SLAT , R FLAP – 12 VDC “A”
54		R SLAT L FLAP – 12VDC “B”
9		-12 ?????
21		-6.25 VOLTS AOA



Remove power wire from T1 to s28 and s34 to reduce rats nest.

Remove wire from L36 to facilitate relocation and to reduce rats nest. Now it is a blue wire from Tb 13 to L36-BLK and s33-10

Troubleshoot and repair SLAT STALL circuit.

Troubleshoot and repair FLAP command switch circuit, and Flap handle interface.

Troubleshoot and repair FLAP UP and FLAP RUN circuit.

Troubleshoot and repair SLAT RUN circuit.

Replace VeederRoot timer with New-Old-Stock. Purchased new old stock

Secure relays with tie string method.

Remove and replace 5 switches with new.
Replaced sw s34 with new.

8 switches test high resistance.. S3, S5 s6, s7 replaced with new and rewire. Rewire T1 and T2 to Rac switch area to reduce rats nest.

Replace FLAP 40 select switch with new. Replace with Dpdt LOCKING

Replace 1 relay with new

Modify unit for Gulfstream G150 / BRAKE DELAY feature. This will add a single switch, with placarding.
Added new wires and switch. Added placard for test fit.

Modify unit for DELAY DISABLE and NO MOTION disable features. This will add 3 switches with placarding.
Positioned and drilled panel for new placards, test fit.

Rotate BRK RLSD switches 180 degrees and rewire. Fabricate proper placarding with time limit warning.
Rotated switches for RLSD to be up, NORM to be down

Test PDU Temperature volt meters

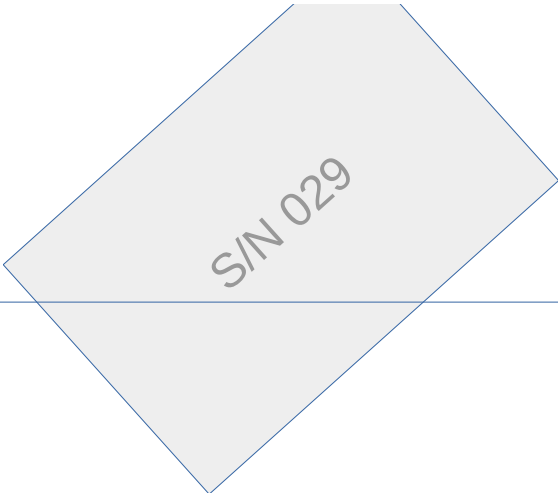
SEPT1 WORKED S RLSD SWITCH REWIRE.

Placards for

Fab Switch guard for G150 W/ DELAY switch

Fill placard letters

Install Volt meters



Remove and replace 8 switches, Replace FLAP 40 select switch with new.
reduce rats nest.
facilitate relocation FLAP RLSD and ANN

Procure and replace eplace VeederRoot timer with New-Old-Stock.

Secure relays with tie string method.

MODS
Modify unit for Gulfstream G150 / BRAKE DELAY feature.with placarding.Fab Switch guard for G150 W/ DELAY switch

Add DELAY DISABLE and NO MOTION disable features.
Rotate BRK RLSD switches 180 degrees and rewire.
Test PDU Temperature volt meters
Add Voltmeters

Add placardsPlacards for

Fill placard letters

Install Volt meters wit face placard
Add EXT switch for +12v

_____ -

Add box
Laminate prints
ADD new G150 prints
Tubes



CAVEATS

Must run on an airplane before release
ASYM flap switch wire is not like trimec box.

